

CIRCUITS, LOWER BOUNDS,
AND
CIRCUIT ANALYSIS ALGORITHMS

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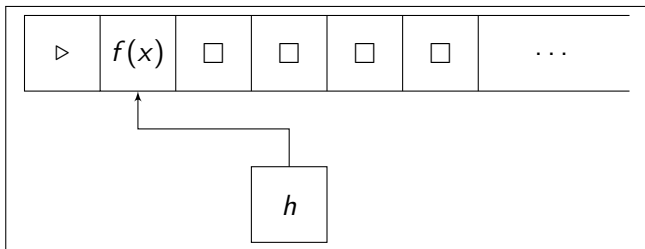
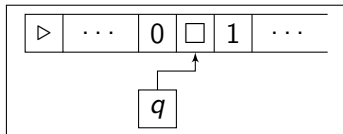
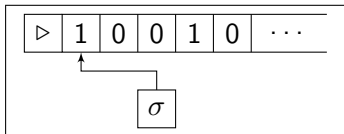
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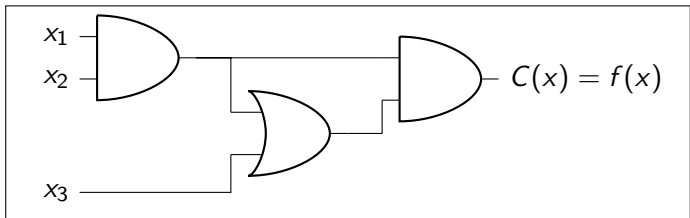
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Introduction

$$f : \{0, 1\}^n \rightarrow \{0, 1\}$$





Definition

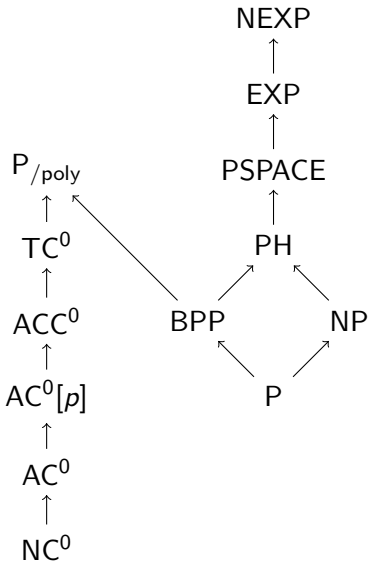
$$P = \bigcup_{k \in \mathbb{N}} \text{TIME}(n^k).$$

Definition

$$P_{/\text{poly}} = \bigcup_{k \in \mathbb{N}} \text{SIZE}(n^k).$$

Lemma

$$P \subsetneq P_{/\text{poly}}.$$



Circuit Lower Bounds

Question (OPEN)

$$NP \stackrel{?}{\subseteq} P.$$

Question (OPEN)

$$NP \stackrel{?}{\subseteq} P_{/poly}.$$

Question

$NP \stackrel{?}{\subseteq} NC^0$.

Lemma

$NP \not\subseteq NC^0$.

Question

$NP \stackrel{?}{\subseteq} AC^0$.

Theorem ([FSS81, Ajt83, Yao85, Hås86])

$\oplus \notin AC^0$.

Corollary

$NP \not\subseteq AC^0$.

Question

$NP \stackrel{?}{\subseteq} AC^0[p]$.

Theorem ([Raz87, Smo87])

$MOD_q \notin AC^0[p]$.

Corollary

$NP \not\subseteq AC^0[p]$.

Question (OPEN)

$NP \stackrel{?}{\subseteq} ACC^0$.

Theorem ([MW18])

$NQuasiP \not\subseteq ACC^0$.

Circuit Analysis Notions

Definition (Satisfiability)

Input : A Boolean circuit $C : \{0, 1\}^n \rightarrow \{0, 1\}$ from a circuit class \mathcal{C} .

Question : Is there any $x \in \{0, 1\}^n$ such that $C(x) = 1$?

Definition (Minimum Circuit Size Problem [KC00])

Input : The truth table $\text{tt}(f) \in \{0, 1\}^{2^n}$ of a Boolean function $f : \{0, 1\}^n \rightarrow \{0, 1\}$ and an integer $1 \leq s \leq 2^n$.

Question : Is there any Boolean circuit $C : \{0, 1\}^n \rightarrow \{0, 1\}$ of size $|C| \leq s$ that computes f ?

Definition (Learning [Val84, KV94])

Let $0 < \varepsilon, \delta < 1$.

Input : An oracle for some Boolean function $f : \{0, 1\}^n \rightarrow \{0, 1\}$ from a circuit class \mathcal{C} .

Output : A circuit $h : \{0, 1\}^n \rightarrow \{0, 1\}$, such that

$$\Pr_{x \sim \{0,1\}^n} [h(x) \neq f(x)] \leq \varepsilon,$$

with probability at least $1 - \delta$.

Definition (Compression [CKK⁺14])

Input : The truth table $\text{tt}(f) \in \{0,1\}^{2^n}$ of some Boolean function $f : \{0,1\}^n \rightarrow \{0,1\}$ from a circuit class \mathcal{C} .

Output : A circuit C of size $o(2^n/n)$ that computes f .

Definition (Natural Properties [RR94])

Let

- \mathcal{F}_n denote the set of all Boolean functions on n variables,
- D be some complexity class, and
- $s : \mathbb{N} \rightarrow \mathbb{N}$ is a size function.

A D -natural property useful against $\mathcal{C}[s]$ is an algorithm

$A : \{0, 1\}^{2^n} \rightarrow \{0, 1\}$ such that

1. A is computable in D ,
2. if $f \in \mathcal{C}[s]$, then $A(\text{tt}(f)) = 0$, and
3. $\Pr_{g \sim \mathcal{F}_n}[A(\text{tt}(g)) = 1] \geq \frac{1}{2}$.

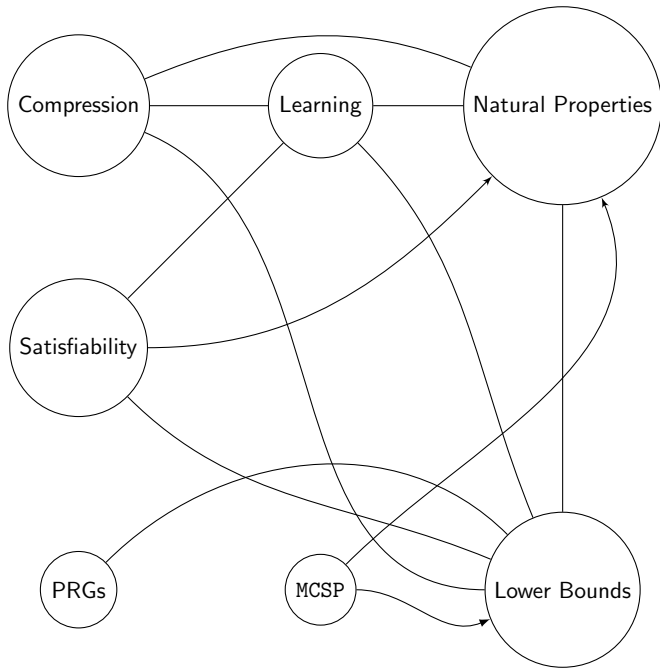
Definition (PRGs)

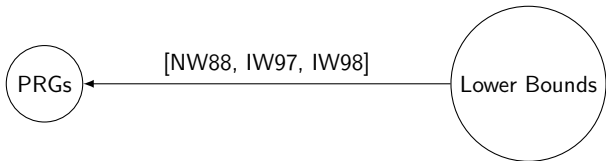
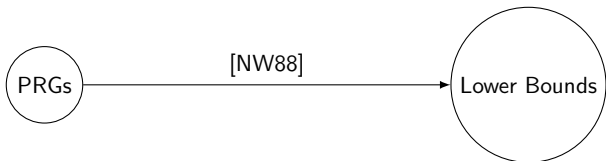
Let \mathcal{F} be a class of functions on n variables and $\varepsilon > 0$. We say that $G : \{0, 1\}^s \rightarrow \{0, 1\}^n$ is a PRG that ε -fools \mathcal{F} if

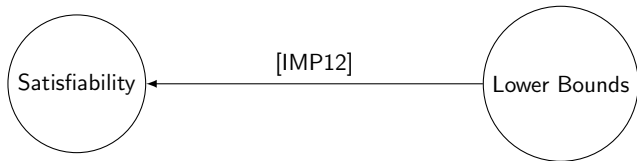
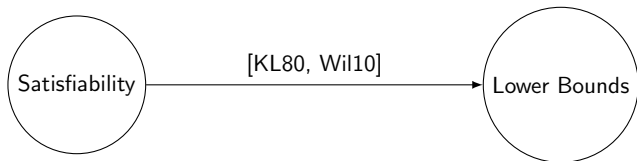
$$\left| \Pr_{y \sim \{0,1\}^s} [f(G(y)) = 1] - \Pr_{x \sim \{0,1\}^n} [f(x) = 1] \right| \leq \varepsilon,$$

for all $f \in \mathcal{F}$.

Known Connections

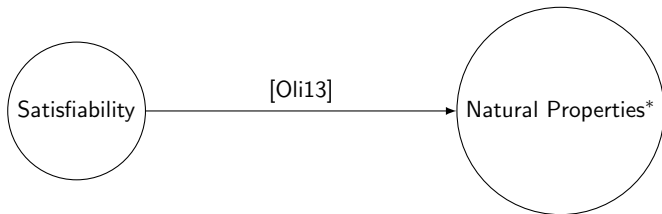


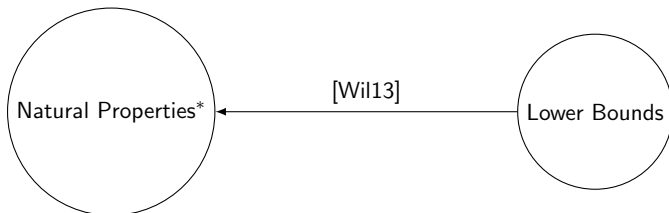
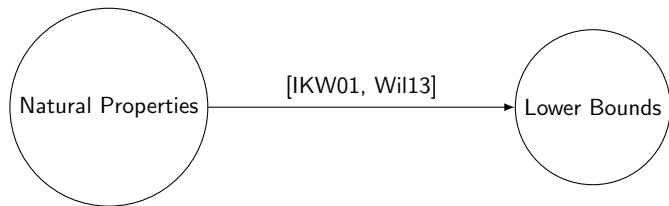


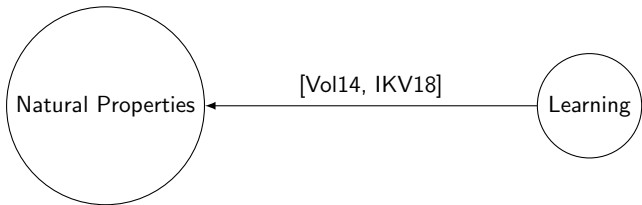
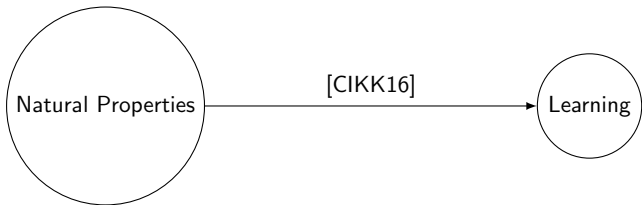


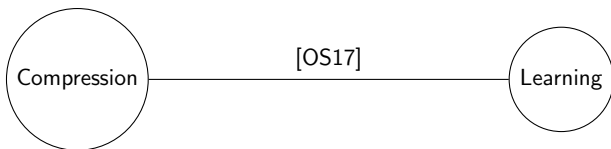


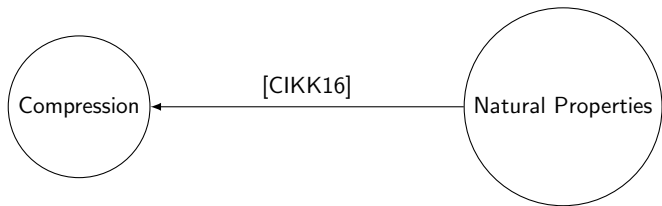


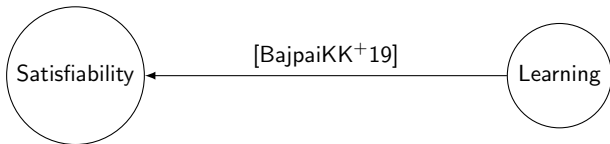
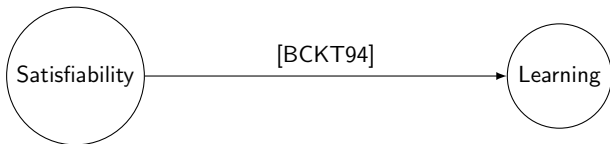


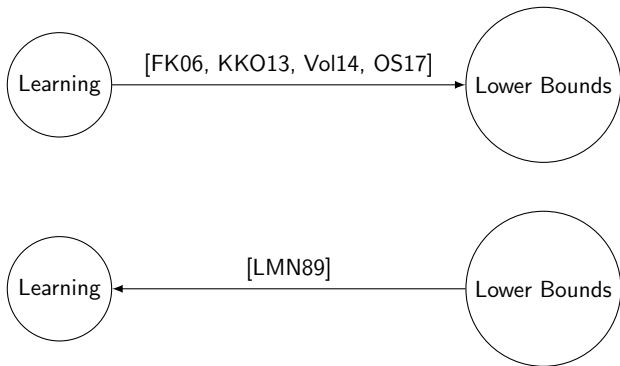


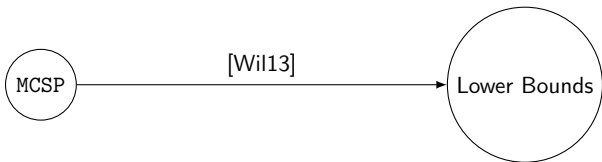


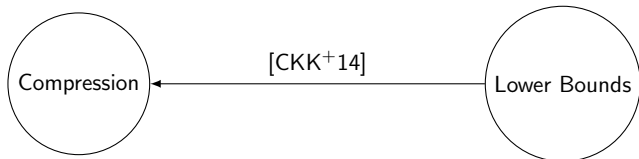
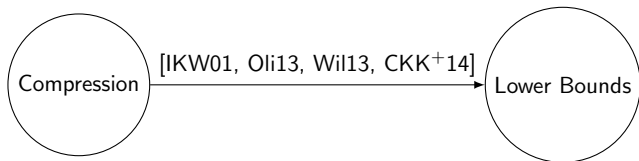






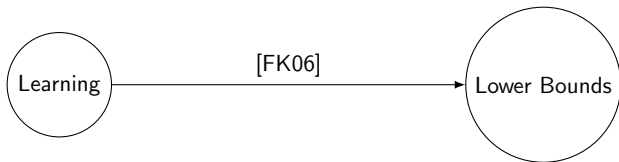






Example: Learning to Lower Bounds





Theorem ([FK06])

Let $s : \mathbb{N} \rightarrow \mathbb{N}$ be a (polynomially-bounded) size function.

Let $\mathcal{C}[s] \subseteq P_{/\text{poly}}$ be a circuit class exactly learnable in time $t := 2^{s^{o(1)}}$ from membership and equivalence queries.

Then, $\text{EXP}^{\text{NP}} \not\subseteq \mathcal{C}[s]$.

Note: The learning algorithm runs in subexponential time.

Definition

Let $f \in \mathfrak{C}[s]$ with $f : \{0, 1\}^n \rightarrow \{0, 1\}$.

$$\forall x \in \{0, 1\}^n : \text{MQ}(x) = f(x).$$

Definition

Let $f \in \mathcal{C}[s]$ with $f : \{0, 1\}^n \rightarrow \{0, 1\}$.

For all hypotheses (circuits) $h : \{0, 1\}^n \rightarrow \{0, 1\}$ we have that

$$\text{EQ}(h) = \begin{cases} 1, & \text{if } h(x) = f(x) \text{ for all } x \in \{0, 1\}^n, \\ y : h(y) \neq f(y), & \text{otherwise.} \end{cases}$$

Theorem ([FK06])

Let $s : \mathbb{N} \rightarrow \mathbb{N}$ be a (polynomially-bounded) size function.

Let $\mathcal{C}[s] \subseteq P_{/\text{poly}}$ be a circuit class exactly learnable in time $t := 2^{s^{o(1)}}$ from membership and equivalence queries.

Then, $\text{EXP}^{\text{NP}} \not\subseteq \mathcal{C}[s]$.

Note: The learning algorithm runs in subexponential time.

Proof.

1. Assume that $\text{EXP}^{\text{NP}} \subseteq \mathcal{C}[s] \subseteq \text{P}_{/\text{poly}}$. (*)
2. This implies $\text{EXP}^{\text{NP}} = \text{P}^{\#\text{P}}$.
3. Thus PERM is complete for EXP^{NP} .
4. Now use the (time- t) learning algorithm to show

$$\text{PERM} \in \text{SUBEXP}^{\text{NP}} := \text{TIME}\left(2^{n^{o(1)}}\right)^{\text{NP}}.$$

5. This yields $\text{EXP}^{\text{NP}} \subseteq \text{SUBEXP}^{\text{NP}}$. A contradiction!

Proof.

1. Assume that $\text{EXP}^{\text{NP}} \subseteq \mathcal{C}[s] \subseteq \text{P}_{/\text{poly}}$. (*)
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Proof (continued).

Assume that $\text{EXP}^{\text{NP}} \subseteq \mathfrak{C}[s] \subseteq P_{/\text{poly}}$.

This implies $\text{EXP}^{\text{NP}} = P^{\#P}$, as

$$\text{EXP}^{\text{NP}} \subseteq P_{/\text{poly}} \stackrel{[\text{BH92}]}{\implies} \text{EXP}^{\text{NP}} = \text{EXP},$$

$$\text{EXP} \subseteq P_{/\text{poly}} \stackrel{[\text{BN91}]}{\implies} \text{EXP} = \text{MA},$$

and

$$\text{MA} \subseteq \text{PH} \subseteq P^{\#P}.$$

Proof (continued).

1. Assume that $\text{EXP}^{\text{NP}} \subseteq \mathcal{C}[s] \subseteq \text{P}_{/\text{poly}}$. (*)
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Proof (continued).

We will use the (time- t) learning algorithm to show

$$\text{PERM} \in \text{SUBEXP}^{\text{NP}},$$

by induction on i .

Here, i refers to the input matrices size, namely $i \times i$.

Proof (continued).

Input: x ; a $n \times n$ Boolean matrix.

For $i \leftarrow 1$ to n :

- a. If $i = 1$, then output the trivial circuit for PERM on 1×1 matrices; else
- b. Run the (time- t) exact learning algorithm to find C_i , the circuit that computes PERM on $i \times i$ matrices.
- c. Simulate MQ and EQ using C_{i-1} and an NP oracle.

Output: $C_n(x) = \text{PERM}(x)$.

Proof (continued).

Input: x ; a $n \times n$ Boolean matrix.

For $i \leftarrow 1$ to n :

- a. If $i = 1$, then output the trivial circuit for PERM on 1×1 matrices; else
- b. Run the (time- t) exact learning algorithm to find C_i , the circuit that computes PERM on $i \times i$ matrices.
- c. **Simulate MQ and EQ using C_{i-1} and an NP oracle.**

Output: $C_n(x) = \text{PERM}(x)$.

Proof (continued).

Simulate $\text{MQ}(y) = \text{PERM}(y)$, with $|y| = i$, using C_{i-1} .

Self-reduction: $\text{PERM}(y)$ can be computed by i calls to C_{i-1} .

This runs in polynomial (in n) time as $i \leq n$ and C_{i-1} is of polynomial (in n) size.

Proof (continued).

Simulate $\text{EQ}(h)$, with $h : \{0, 1\}^i \rightarrow \{0, 1\}$, using C_{i-1} and an NP oracle:

i. Ask the NP oracle:

“Does there exist a $z \in \{0, 1\}^i$ such that $h(z) \neq \text{PERM}(z)$?”

ii. If NO, then $h(x) = \text{PERM}(x)$ for all $x \in \{0, 1\}^i$.

iii. If YES, then find z . Ask the NP oracle:

“Does there exist a $z \in \{0, 1\}^{i-1}$ such that $h(z) \neq \text{PERM}(z)$?”

Repeat until all i bits of z are computed.

This runs in polynomial (in n) time as $i \leq n$ and h and C_{i-1} are of polynomial (in n) size.

Proof (continued).

Input: x ; a $n \times n$ Boolean matrix.

For $i \leftarrow 1$ to n :

- a. If $i = 1$, then output the trivial circuit for PERM on 1×1 matrices; else
- b. Run the (time- t) exact learning algorithm to find C_i , the circuit that computes PERM on $i \times i$ matrices.
- c. Simulate MQ and EQ using C_{i-1} and an NP oracle.

Output: $C_n(x) = \text{PERM}(x)$.

Proof (continued).

What is the running time?

The running time of the previous procedure is

$$\text{poly}(n, t) = \text{poly}\left(n, 2^{s^{o(1)}}\right).$$

Note that $s = \text{poly}(n)$; this yields $\text{PERM} \in \text{SUBEXP}^{\text{NP}}$.

Proof.

1. Assume that $\text{EXP}^{\text{NP}} \subseteq \mathcal{C}[s] \subseteq \text{P}_{/\text{poly}}$. (*)
2. This implies $\text{EXP}^{\text{NP}} = \text{P}\#\text{P}$.
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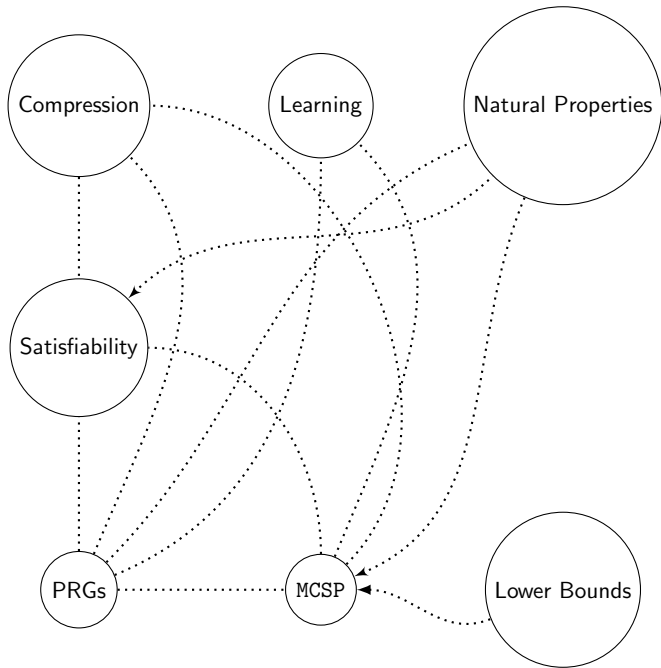
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5. This yields $\text{EXP}^{\text{NP}} \subseteq \text{SUBEXP}^{\text{NP}}$. A contradiction!

The proof is complete.



Discussion: OPEN Problems



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Acknowledgements II

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Thank you!

References

- [Ajt83] M. Ajtai.
 Σ_1^1 -Formulae on Finite Structures.
Annals of Pure and Applied Logic, 24(1):1–48, 1983.
- [BCKT94] Nader H. Bshouty, Richard Cleve, Sampath Kannan, and
Christino Tamon.
Oracles and queries that are sufficient for exact learning
(extended abstract).
In *Proceedings of the Seventh Annual ACM Conference
on Computational Learning Theory, COLT 1994, New
Brunswick, NJ, USA, July 12-15, 1994.*, pages 130–139,
1994.

References

- [BH92] Harry Buhrman and Steven Homer.
Superpolynomial Circuits, Almost Sparse Oracles and
the Exponential Hierarchy.
In *FSTTCS*, volume 652 of *Lecture Notes in Computer
Science*, pages 116–127. Springer, 1992.
- [BN91] László Babai and Noam Nisan.
BPP has Subexponential Time Simulation unless
EXPTIME has Publishable Proofs.
In *Proceedings of the Sixth Annual Structure in
Complexity Theory Conference, Chicago, Illinois, USA,
June 30 - July 3, 1991*, pages 213–219, 1991.

References

- [CIKK16] Marco L. Carmosino, Russell Impagliazzo, Valentine Kabanets, and Antonina Kolokolova.
Learning Algorithms from Natural Proofs.
In 31st Conference on Computational Complexity, CCC 2016, May 29 to June 1, 2016, Tokyo, Japan, pages 10:1–10:24, 2016.
- [CKK⁺14] Ruiwen Chen, Valentine Kabanets, Antonina Kolokolova, Ronen Shaltiel, and David Zuckerman.
Mining Circuit Lower Bound Proofs for Meta-algorithms.
In IEEE 29th Conference on Computational Complexity, CCC 2014, Vancouver, BC, Canada, June 11-13, 2014, pages 262–273, 2014.

References

- [FK06] Lance Fortnow and Adam R. Klivans.
Efficient Learning Algorithms Yield Circuit Lower
Bounds.
*In Learning Theory, 19th Annual Conference on Learning
Theory, COLT 2006, Pittsburgh, PA, USA, June 22-25,
2006, Proceedings*, pages 350–363, 2006.
- [FSS81] Merrick L. Furst, James B. Saxe, and Michael Sipser.
Parity, Circuits, and the Polynomial-Time Hierarchy.
*In 22nd Annual Symposium on Foundations of
Computer Science, Nashville, Tennessee, USA, 28-30
October 1981*, pages 260–270, 1981.

References

- [Hås86] J. Håstad.
Almost Optimal Lower Bounds for Small Depth Circuits.
In *Proceedings of the Eighteenth Annual ACM Symposium on Theory of Computing, STOC '86*, pages 6–20, New York, NY, USA, 1986. ACM.
- [IKV18] Russell Impagliazzo, Valentine Kabanets, and Ilya Volkovich.
The Power of Natural Properties as Oracles.
In *33rd Computational Complexity Conference, CCC 2018, June 22-24, 2018, San Diego, CA, USA*, pages 7:1–7:20, 2018.

References

- [IKW01] Russell Impagliazzo, Valentine Kabanets, and Avi Wigderson.
In Search of an Easy Witness: Exponential Time vs. Probabilistic Polynomial Time.
In Proceedings of the 16th Annual IEEE Conference on Computational Complexity, Chicago, Illinois, USA, June 18-21, 2001, pages 2–12, 2001.
- [IMP12] Russell Impagliazzo, William Matthews, and Ramamohan Paturi.
A Satisfiability Algorithm for AC^0 .
In Proceedings of the Twenty-Third Annual ACM-SIAM Symposium on Discrete Algorithms, SODA 2012, Kyoto, Japan, January 17-19, 2012, pages 961–972, 2012.

References

- [IW97] Russell Impagliazzo and Avi Wigderson.
P = BPP if E Requires Exponential Circuits:
Derandomizing the XOR Lemma.
*In Proceedings of the Twenty-Ninth Annual ACM
Symposium on the Theory of Computing, El Paso,
Texas, USA, May 4-6, 1997, pages 220–229, 1997.*
- [IW98] Russell Impagliazzo and Avi Wigderson.
Randomness vs. Time: De-Randomization under a
Uniform Assumption.
*In 39th Annual Symposium on Foundations of Computer
Science, FOCS '98, November 8-11, 1998, Palo Alto,
California, USA, pages 734–743, 1998.*

References

- [KC00] Valentine Kabanets and Jin-Yi Cai.
Circuit Minimization Problem.
In Proceedings of the Thirty-Second Annual ACM Symposium on Theory of Computing, May 21-23, 2000, Portland, OR, USA, pages 73–79, 2000.
- [KKO13] Adam R. Klivans, Pravesh Kothari, and Igor Carboni Oliveira.
Constructing Hard Functions Using Learning Algorithms.
In Proceedings of the 28th Conference on Computational Complexity, CCC 2013, K.lo Alto, California, USA, 5-7 June, 2013, pages 86–97, 2013.

References

- [KL80] Richard M. Karp and Richard J. Lipton.
Some Connections Between Nonuniform and Uniform Complexity Classes.
In Proceedings of the 12th Annual ACM Symposium on Theory of Computing, April 28-30, 1980, Los Angeles, California, USA, pages 302–309, 1980.
- [KV94] Michael J. Kearns and Umesh V. Vazirani.
An Introduction to Computational Learning Theory.
MIT Press, 1994.

References

- [LMN89] Nathan Linial, Yishay Mansour, and Noam Nisan.
Constant Depth Circuits, Fourier Transform, and Learnability.
In 30th Annual Symposium on Foundations of Computer Science, Research Triangle Park, North Carolina, USA, 30 October - 1 November 1989, pages 574–579, 1989.
- [MW18] Cody Murray and R. Ryan Williams.
Circuit Lower Bounds for Nondeterministic Quasi-Polytime: An Easy Witness Lemma for NP and NQP.
In STOC, pages 890–901. ACM, 2018.

References

- [NW88] Noam Nisan and Avi Wigderson.
Hardness vs. Randomness (Extended Abstract).
In 29th Annual Symposium on Foundations of Computer Science, White Plains, New York, USA, 24-26 October 1988, pages 2–11, 1988.
- [Oli13] Igor Carboni Oliveira.
Algorithms versus Circuit Lower Bounds.
CoRR, abs/1309.0249, 2013.
- [OS17] Igor Carboni Oliveira and Rahul Santhanam.
Conspiracies Between Learning Algorithms, Circuit Lower Bounds, and Pseudorandomness.

References

In *32nd Computational Complexity Conference, CCC 2017, July 6-9, 2017, Riga, Latvia*, pages 18:1–18:49, 2017.

[Raz87]

Alexander Razborov.

Lower Bounds on the Size of Bounded Depth Circuits Over a Complete Basis with Logical Addition.

Mat. Zametki, 41(4):598–607, 1987.

English translation in *Mathematical Notes of the Academy of Sci. of the USSR*, 41(4):333-338, 1987.

[RR94]

Alexander A. Razborov and Steven Rudich.

Natural Proofs.

In *Proceedings of the Twenty-Sixth Annual ACM Symposium on Theory of Computing, STOC '94*, pages 204–213, New York, NY, USA, 1994. ACM.

References

- [Smo87] Roman Smolensky.
Algebraic Methods in the Theory of Lower Bounds for
Boolean Circuit Complexity.
In *Proceedings of the 19th Annual ACM Symposium on
Theory of Computing, 1987, New York, New York, USA*,
pages 77–82, 1987.
- [Val84] Leslie G. Valiant.
A Theory of the Learnable.
In *Proceedings of the 16th Annual ACM Symposium on
Theory of Computing, April 30 - May 2, 1984,
Washington, DC, USA*, pages 436–445, 1984.

References

- [Vol14] Ilya Volkovich.
On Learning, Lower Bounds and (un)Keeping Promises.
In *Automata, Languages, and Programming - 41st International Colloquium, ICALP 2014, Copenhagen, Denmark, July 8-11, 2014, Proceedings, Part I*, pages 1027–1038, 2014.
- [Wil10] Ryan Williams.
Improving Exhaustive Search Implies Superpolynomial Lower Bounds.
In *Proceedings of the 42nd ACM Symposium on Theory of Computing, STOC 2010, Cambridge, Massachusetts, USA, 5-8 June 2010*, pages 231–240, 2010.

References

- [Wil13] Ryan Williams.
Natural Proofs versus Derandomization.
In Symposium on Theory of Computing Conference, STOC'13, Palo Alto, CA, USA, June 1-4, 2013, pages 21–30, 2013.
- [Yao85] Andrew Chi-Chih Yao.
Separating the Polynomial-Time Hierarchy by Oracles (Preliminary Version).
In 26th Annual Symposium on Foundations of Computer Science, Portland, Oregon, USA, 21-23 October 1985, pages 1–10, 1985.